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SEP 20 2006

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Commissioner for Patents
POB 1450, Alexandria, Virginia 22313-1450
TRANSMITTED TO USPTO MAIN FACSIMILE NO. 571-273-8300

RE: KAJIHARA, et al. - US Appl'n No. 09/328,910
Att'y Docket No. 501.32049R00 / 329201392US2
Examiner J. Clark, AU 2815, USPTO Conf. No. 7455

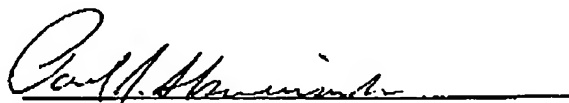
SUBMISSION OF NOTICE OF APPEAL

Sir:

Applicant hereby submits the attached NOTICE OF APPEAL (1 page); and
PETITION FOR EXTENSION OF TIME (1 page), for entry in the above-noted
application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached NOTICE OF APPEAL (1 page); and
PETITION FOR EXTENSION OF TIME (1 page), are being FORMALLY
TRANSMITTED via the USPTO Main Facsimile No. 571-273-8300 on September
20, 2006.


Paul J. Skwierawski

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501.32049R00/329201392US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/328,910

Filed: 9 June 1999

For: LEADFRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: J. Clark

Conf. No.: 7455

NOTICE OF APPEAL

Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

20 September 2006

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the Office Action dated March 20, 2006 of the Primary Examiner, the application having been at least twice rejected.

The items checked below are appropriate:

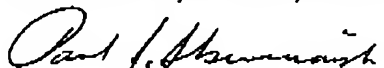
☐ A timely response to the rejection.

☒ Fee \$500.00 ☒ is enclosed ☐ not required
(fee paid in prior appeal).

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (Case No. 1374.32049R00) and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP


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